



# Cache and Data Structures

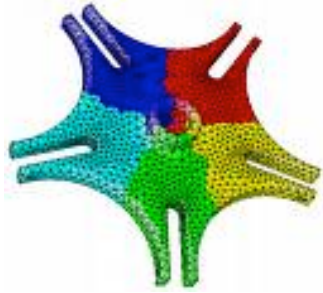
Bruno Raffin

DataMove

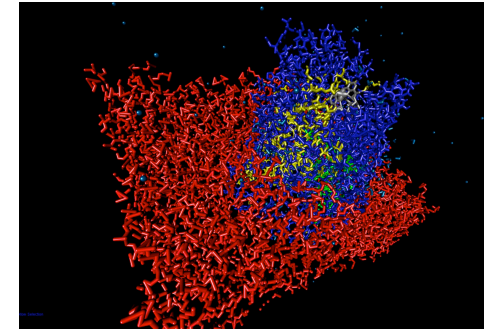
INRIA - LIG

GRENOBLE

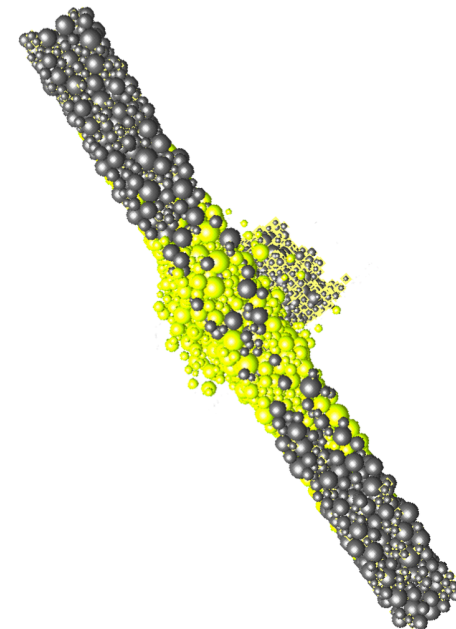
Part of the Slides are from  
Marc Tchiboukdjian  
and  
Marie Durand



## Motivation 1/2



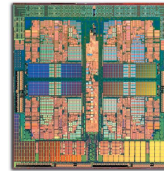
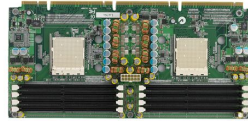
- Numerical simulations:
  - 3D objects: meshes, particles
  - Spatial and temporal coherency
- Computer memories: 1D



# Motivation 2/2

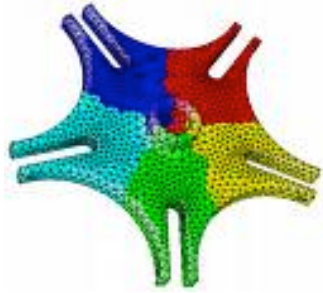
Today's machines:

-> complex memory hierarchies

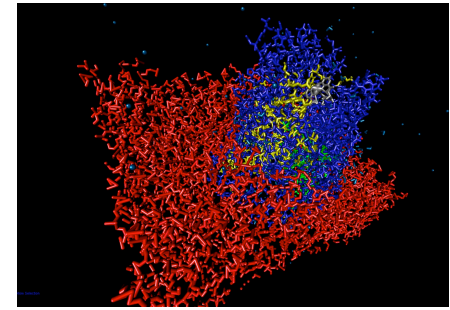


Access by blocks of continuous data (memory pages, cache lines, read/write coalescing)

Need to carefully consider data access schemes and memory layouts



# Spatial Coherency



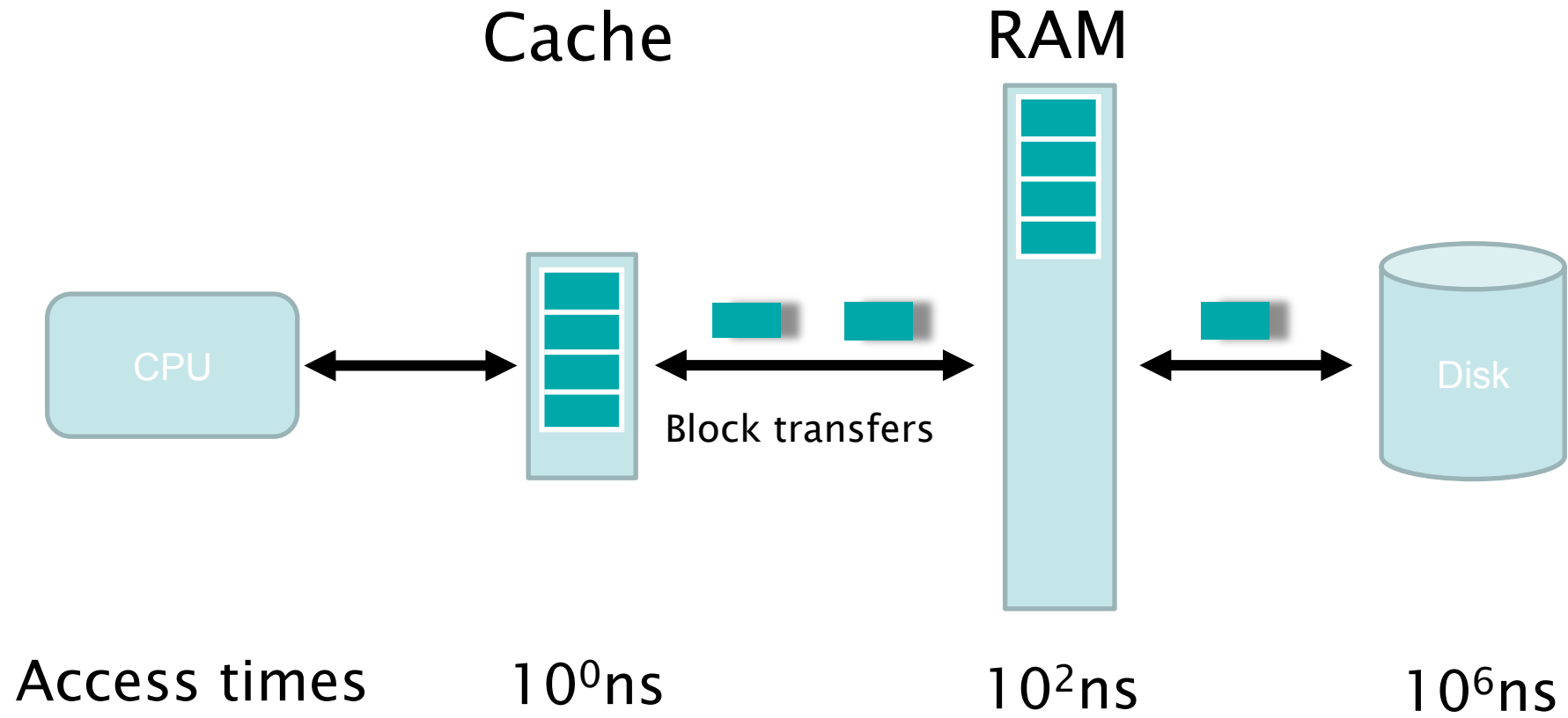
(3D) Neighbor data tend to be accessed together

-> Mesh topology, Atoms, etc.

Try to keep this 3D locality when projecting the data in the 1D memory:

Goal: Access  $n$  neighbor data by  
 $n/B$  memory block transfers ( $B$ -size)

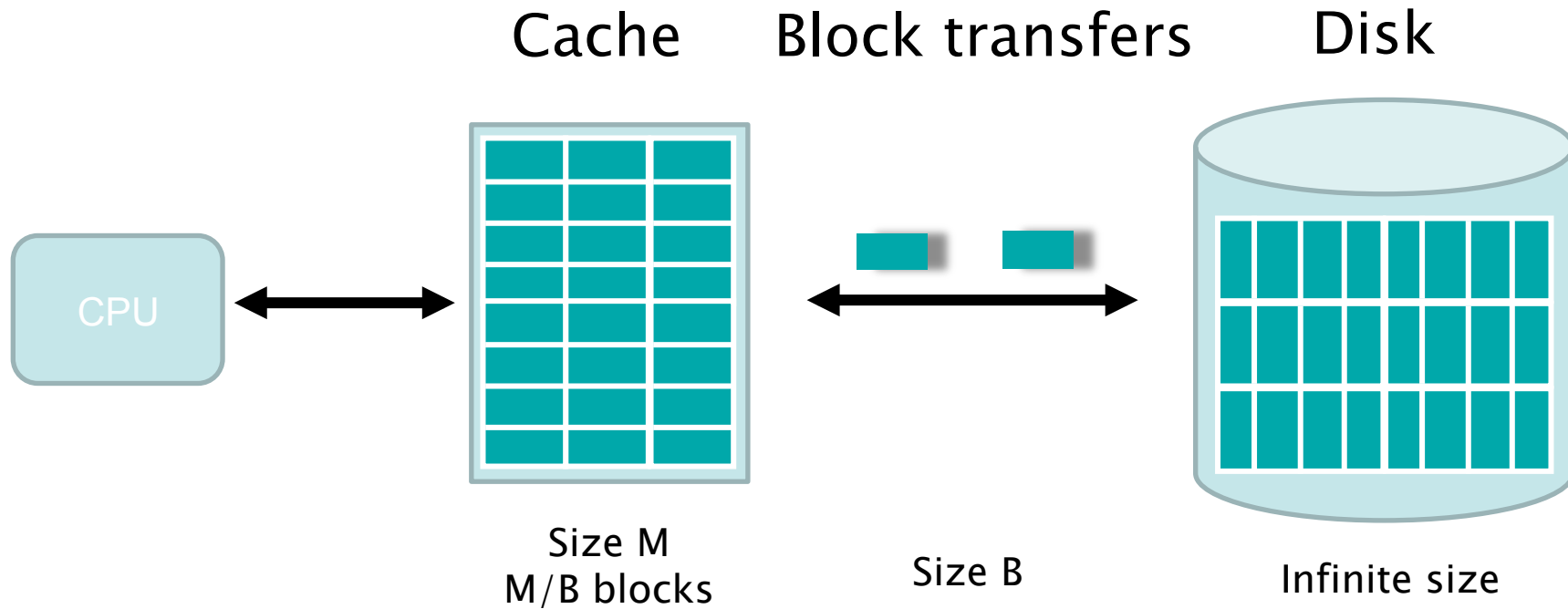
# Memory Hierarchy



# Disk Access Model (DAM)

or external memory  
out-of-core  
cache-aware  
I/O model

[Aggarwal and Vitter 1988]

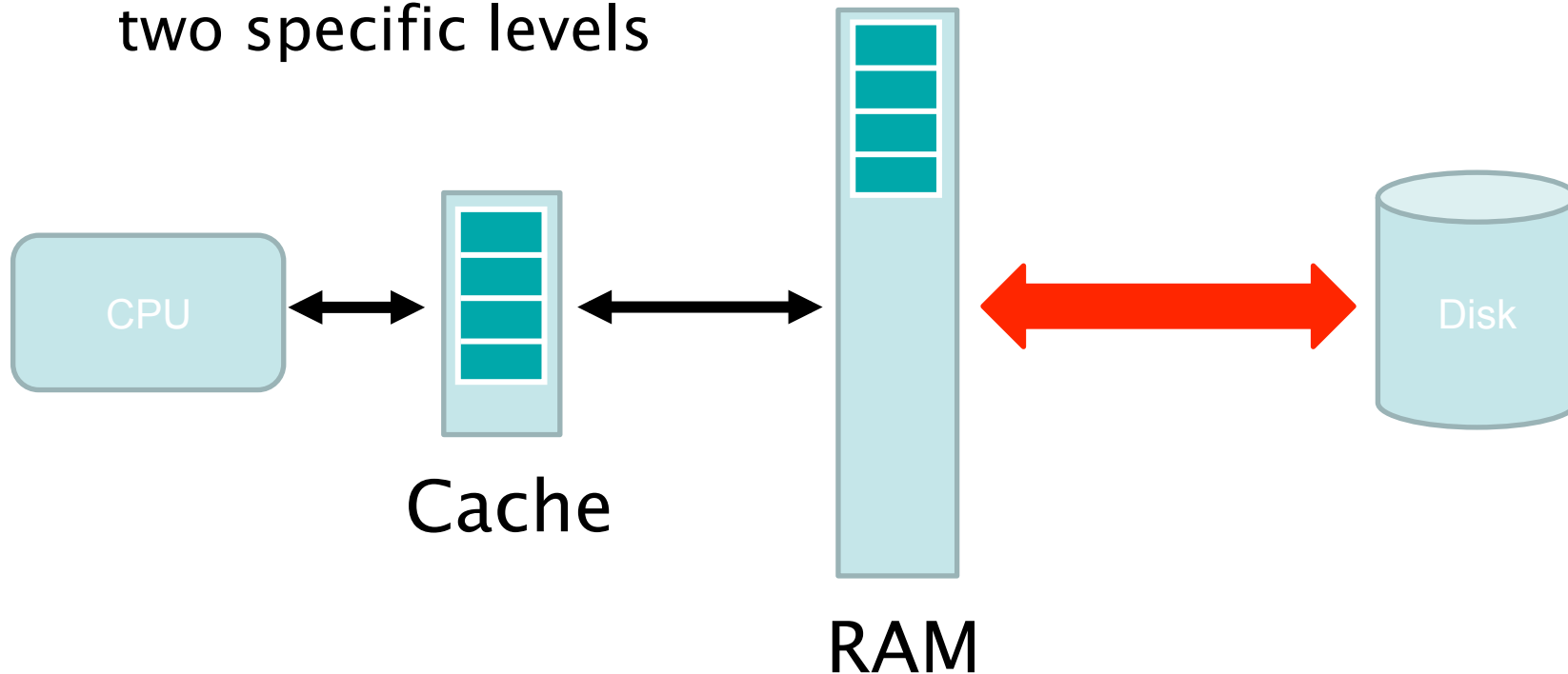


$W$ : #operations CPU

$Q$ : #block transfers

# Advantages of the DAM model

- Simple: only two levels
- Good when the bottleneck is between two specific levels



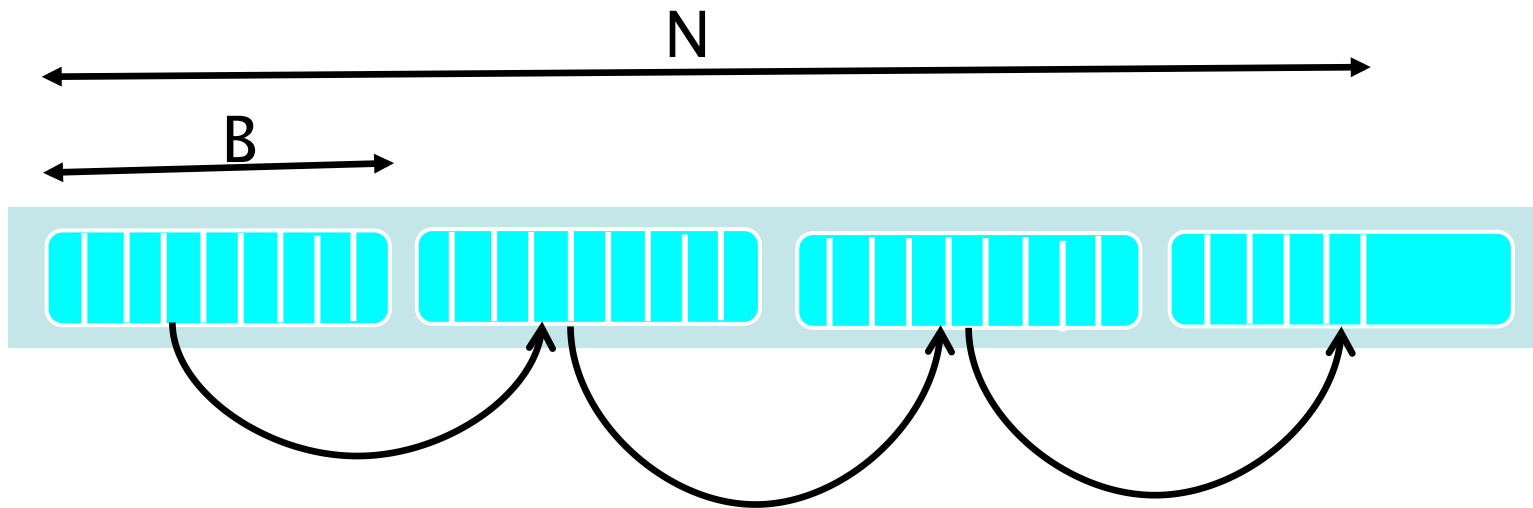


# Principles of external-memory algorithm design

- *Internal efficiency*: work is comparable to the best internal memory algorithms
- *Spatial locality*: a block should contain as much useful data as possible
- *Temporal locality*: as much useful work as possible before the block is ejected

# Scanning in the DAM model

Read an  $N$ -elements array: the naive algorithm is optimal



$$W(N) = N$$

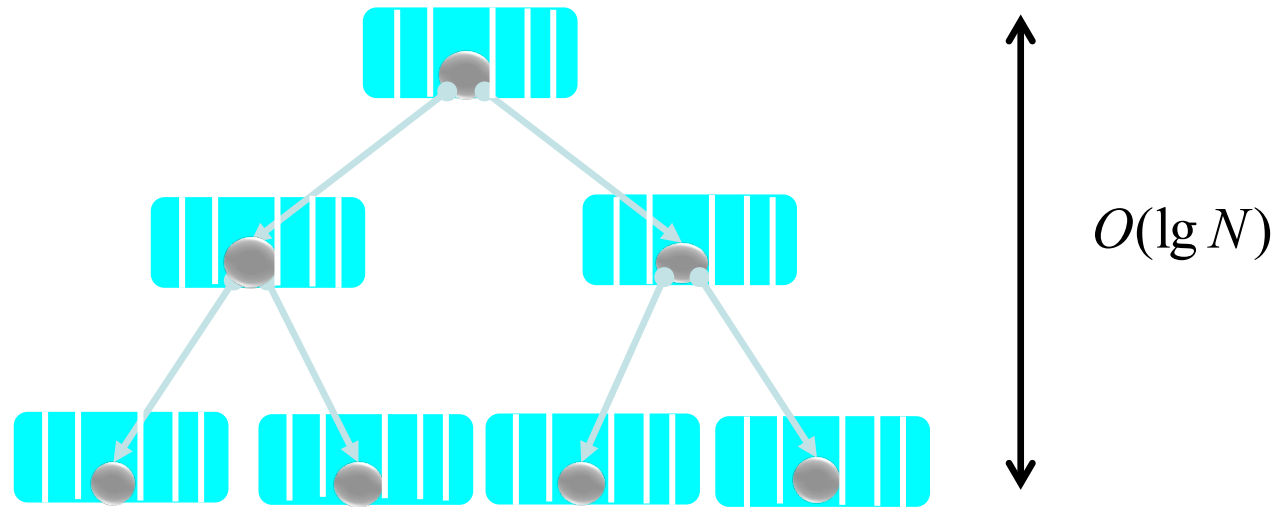
$$Q(N) = \lceil N / B \rceil$$

$$\text{scan}(N) = \lceil N / B \rceil$$

this bound is optimal

# Searching in the DAM model

Searching a key in an  $N$ -nodes balanced binary tree : naive doesn't work



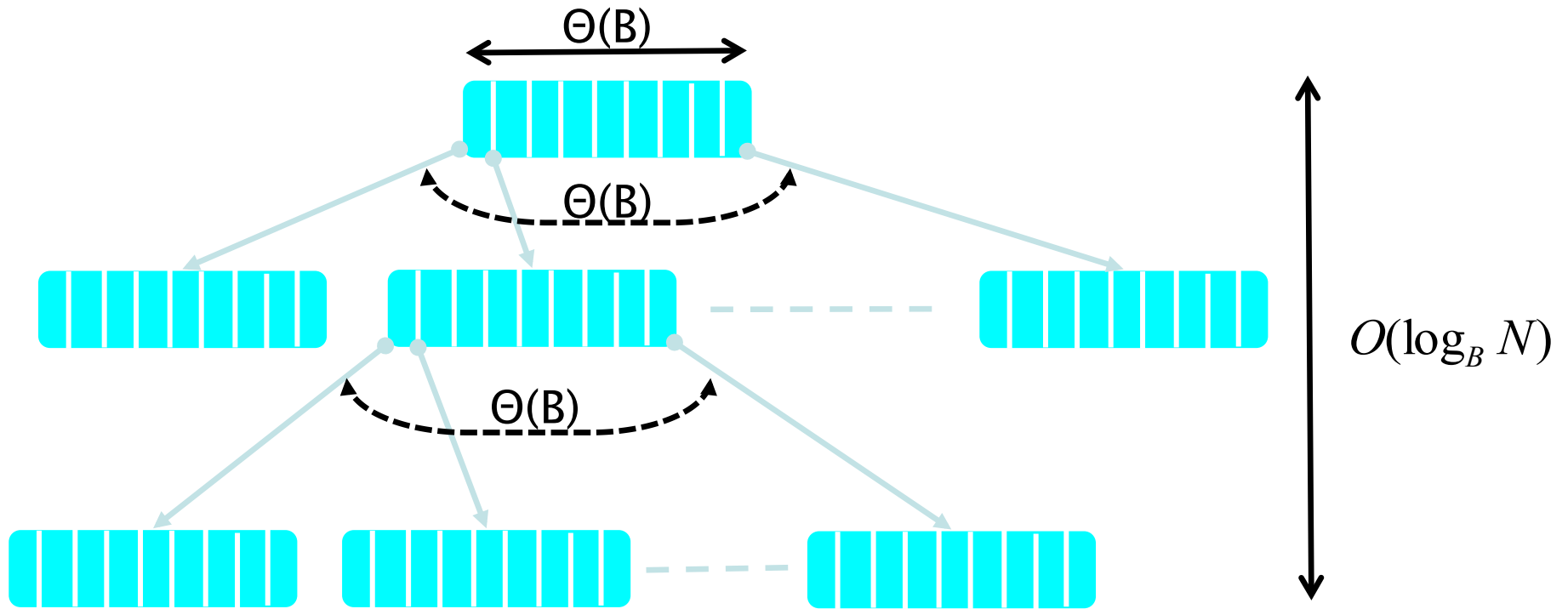
$$W(N) = 1 \cdot O(\lg N) = O(\lg N)$$

$$Q(N) = 1 \cdot O(\lg N) = O(\lg N)$$

# Searching in the DAM model

Searching a key in an N-elements B-tree

[Bayer and McCreight 1972]



$$W(N) = \lg B \cdot O(\log_B N) = O(\lg N)$$

$$Q(N) = 1 \cdot O(\log_B N) = O(\log_B N)$$

# Multiplying in the DAM model

$N \times N$  matrices in row-major order : naive doesn't work

Using the naive  $N^3$  algorithm:

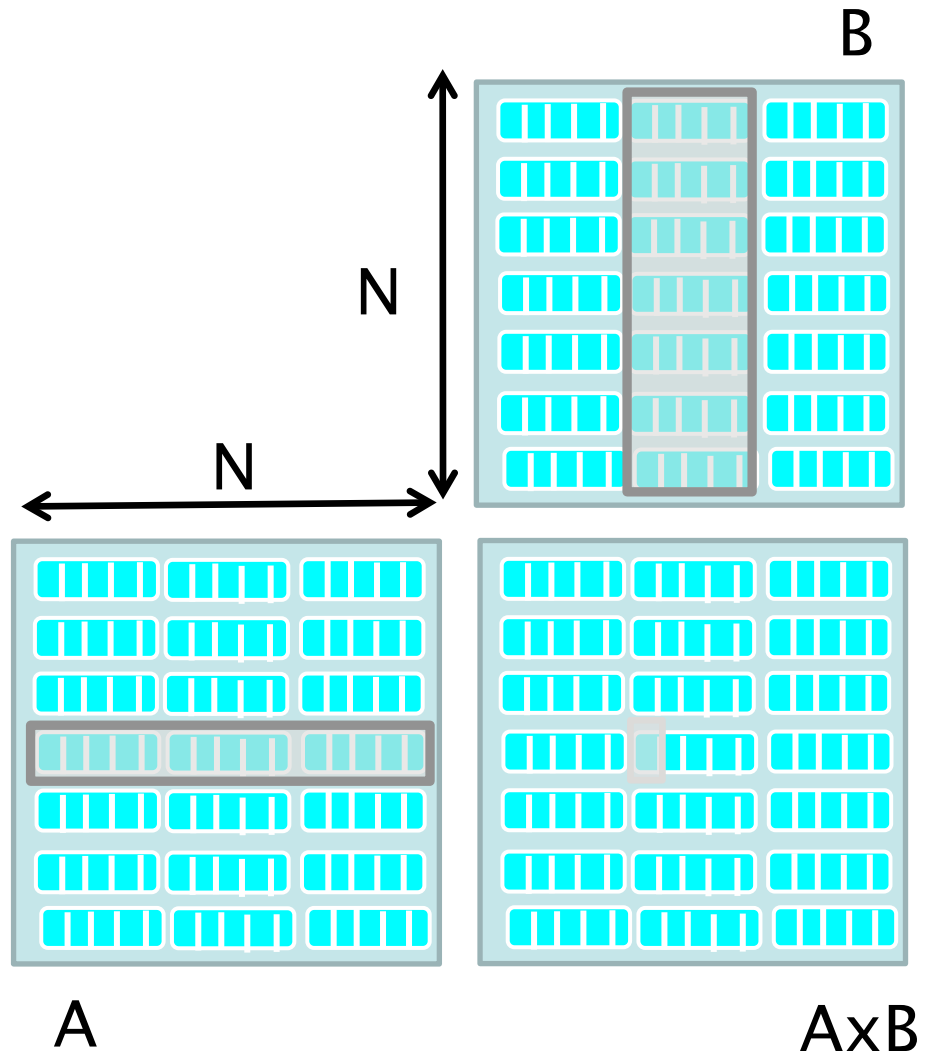
$$W(N) = O(N).N^2$$

$$W(N) = O(N^3)$$

Memory accesses in B are suboptimal:

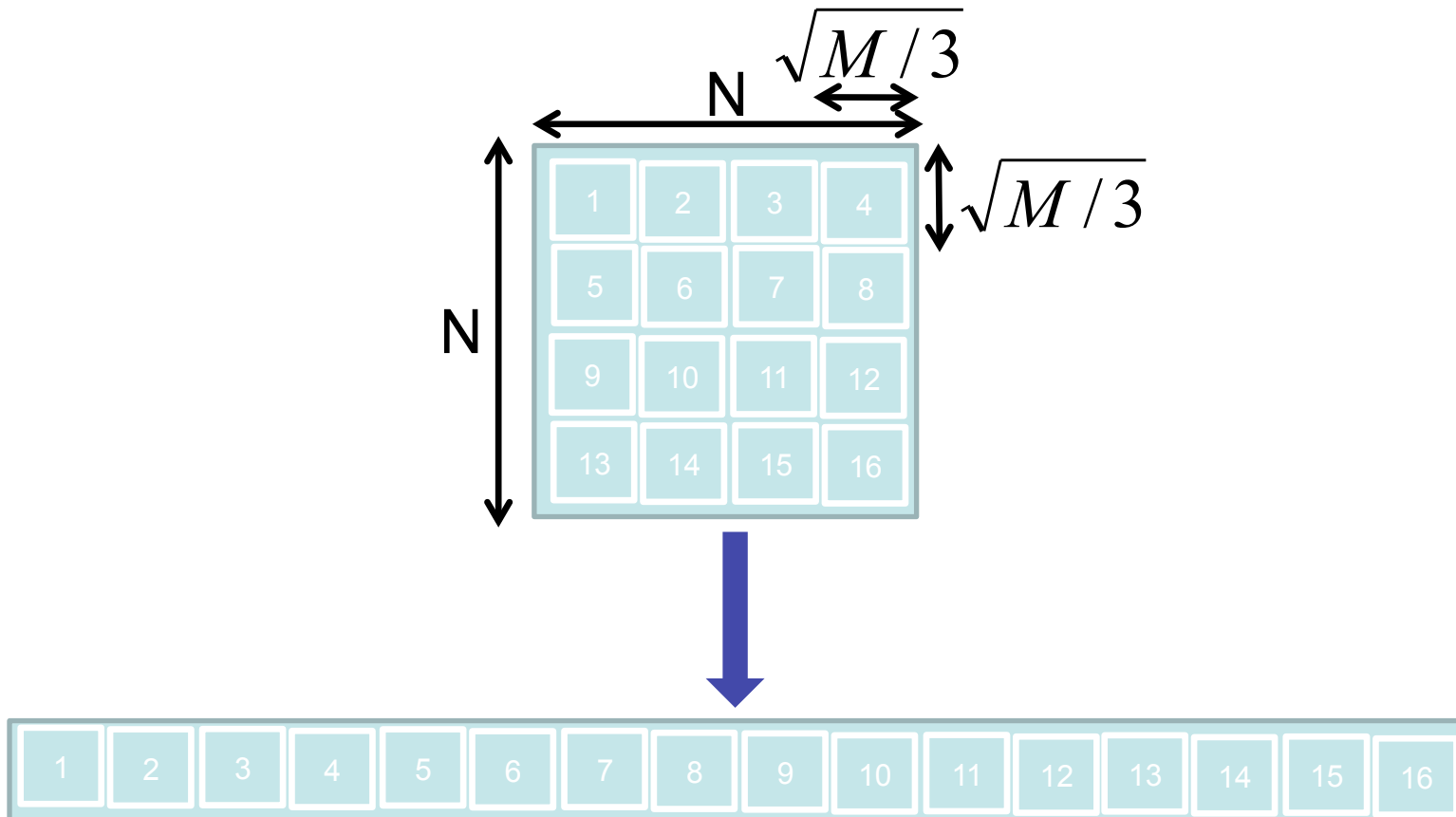
$$Q(N) = O\left(\frac{N}{B} + N\right).N^2$$

$$Q(N) = O(N^3)$$



# Multiplying in the DAM model

$N \times N$  matrices in submatrices



# Multiplying in the DAM model

$N \times N$  matrices in submatrices

- Cost for two sub-matrices

$$W(N) = O\left(\sqrt{M}^3\right) \quad Q(N) = O\left(\frac{M}{B}\right)$$

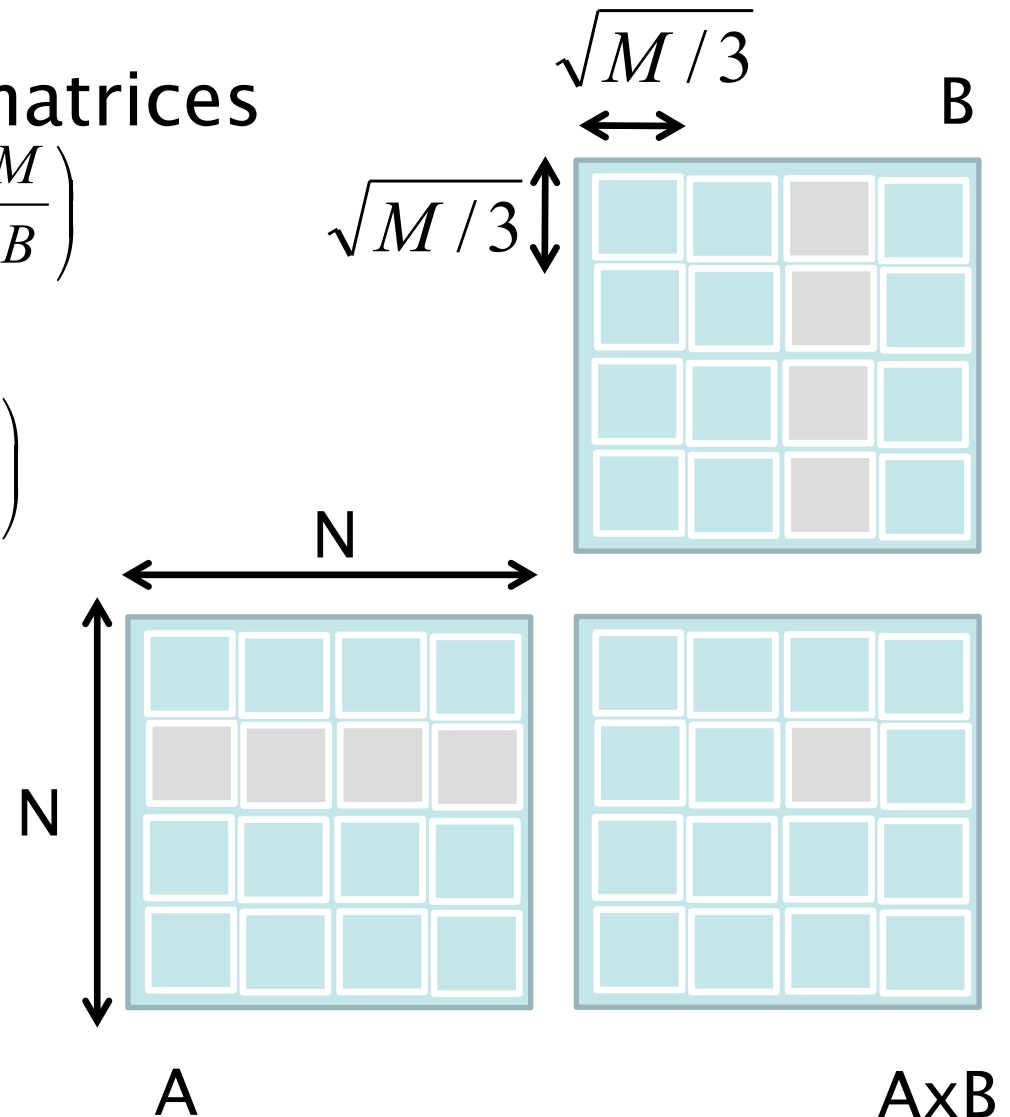
- Total cost

$$W(N) = O\left(\sqrt{M}^3\right) \cdot O\left(\frac{N}{\sqrt{M}}\right) \cdot O\left(\frac{N^2}{M}\right)$$

$$W(N) = O\left(N^3\right)$$

$$Q(N) = O\left(\frac{M}{B}\right) \cdot O\left(\frac{N}{\sqrt{M}}\right) \cdot O\left(\frac{N^2}{M}\right)$$

$$Q(N) = O\left(\frac{N^3}{B\sqrt{M}}\right)$$



# Sorting in the DAM model

M/B-way merge sort of an N-elements array

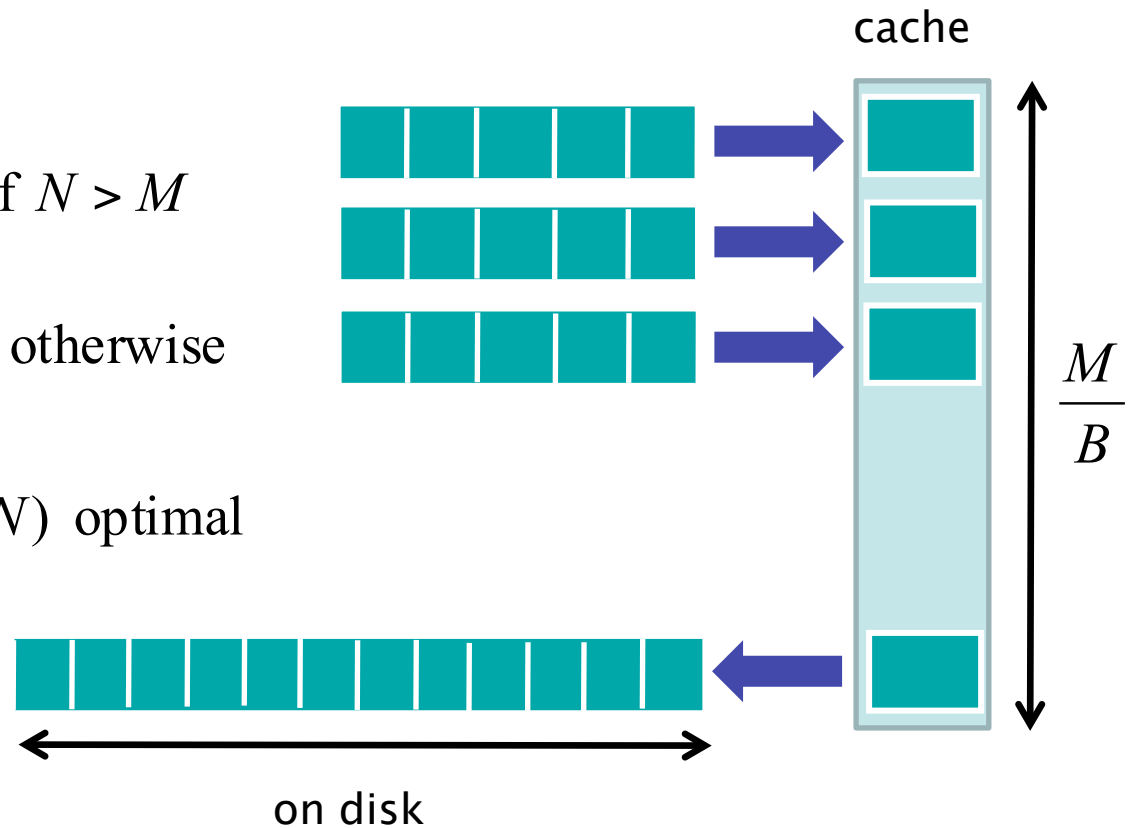
- Cut into M/B sublists
- Recursively sort them
- Merge using a heap of size M/B

$$W(N) = \begin{cases} \frac{M}{B} W\left(\frac{N}{M/B}\right) + N \cdot O\left(\log \frac{M}{B}\right) & \text{if } N > 1 \\ O(1) & \text{otherwise} \end{cases}$$

$$W(N) = O(N \log N)$$

$$Q(N) = \begin{cases} \frac{M}{B} Q\left(\frac{N}{M/B}\right) + O\left(\frac{N}{B}\right) & \text{if } N > M \\ O\left(\frac{N}{B}\right) & \text{otherwise} \end{cases}$$

$$Q(N) = O\left(\frac{N}{B} \log_{M/B} \frac{N}{B}\right) = \text{sort}(N) \text{ optimal}$$





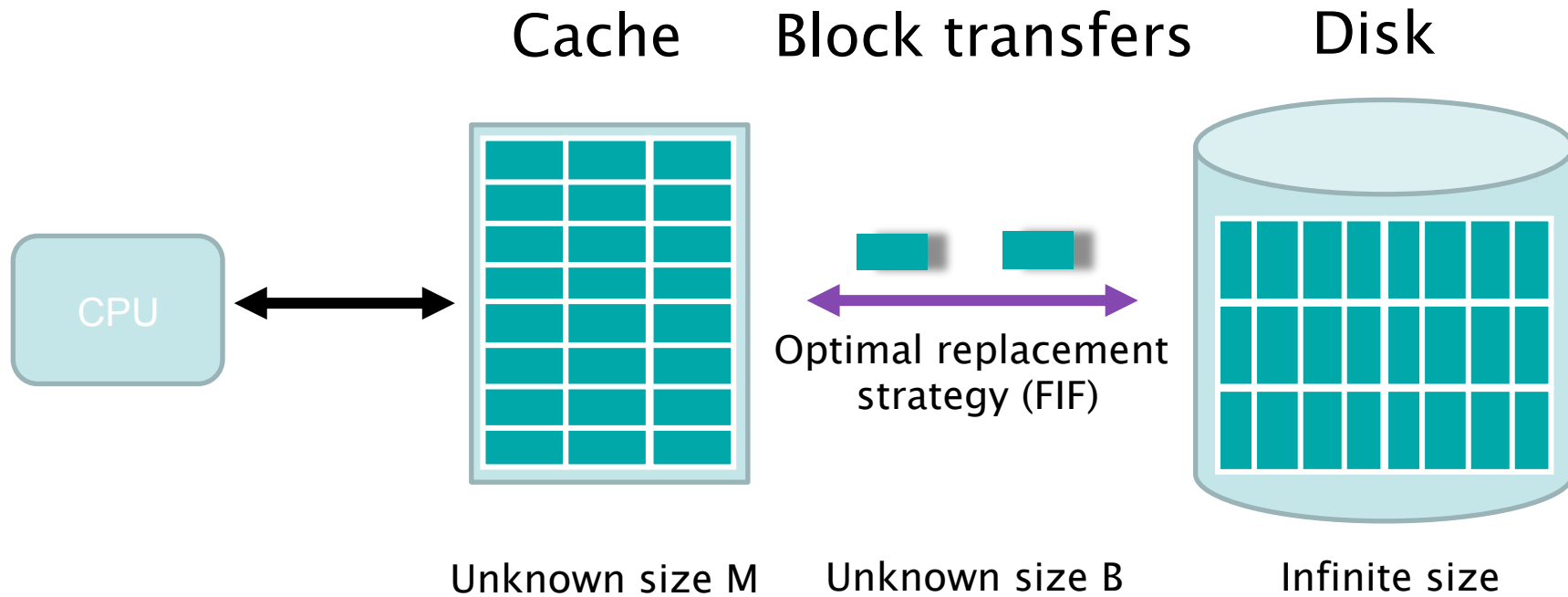
# Limitations of the DAM model

- B and M are needed to design the algorithm
- Only two levels of the hierarchy
- B and M can vary
  - e.g. multi-process scheduling
- Block transfer cost is not uniform
  - disk seek time

**DAM Based Algorithms are said to be “cache-aware”**

# Cache-Oblivious Model (CO)

[Frigo et al 1999]



# Advantages of the CO Model

Parameters are unknown when writing the algorithm (block and cache size):

- Machine-independent
- Efficient with all levels of the memory hierarchy

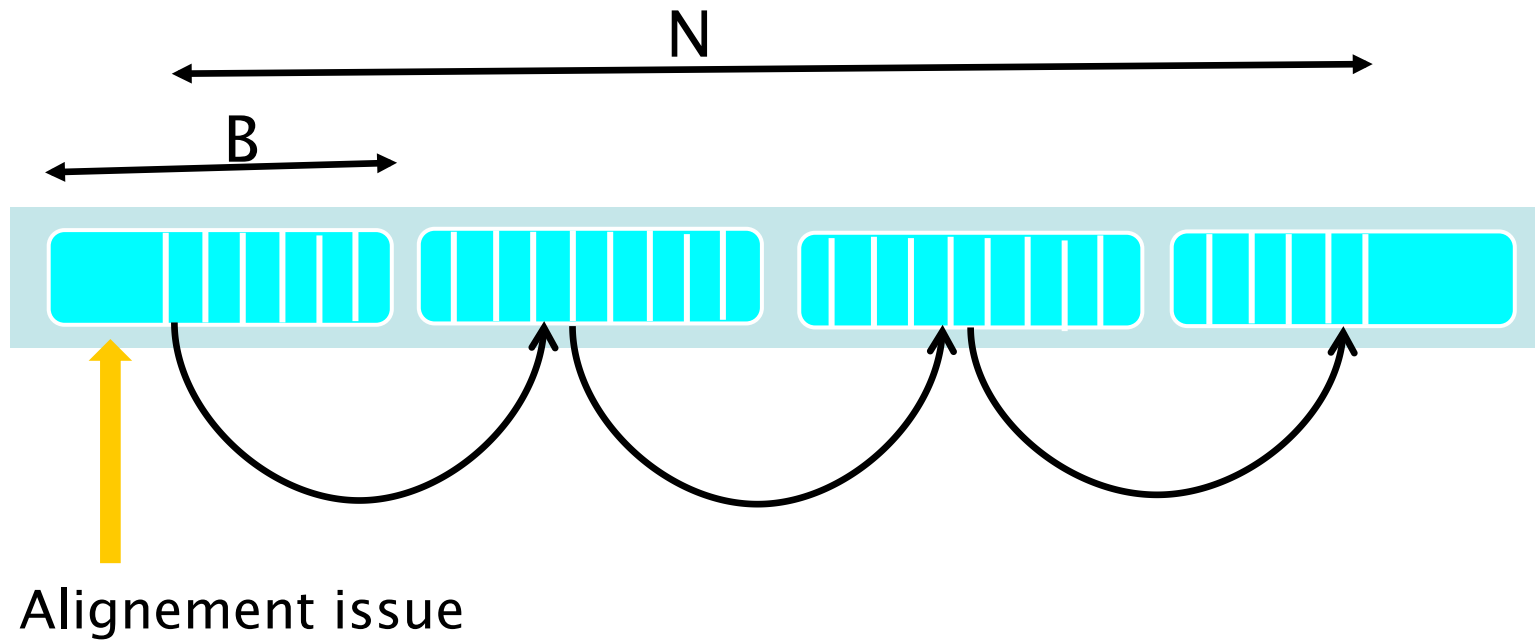
# Assumptions

- Optimal replacement
- Only two levels of memory
- Full associativity
- Tall-cache assumption

$$M = \Omega(B^2)$$

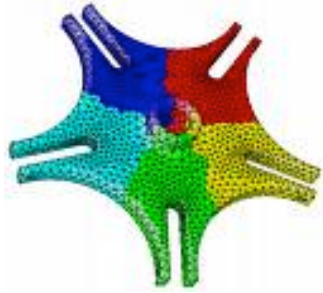
$$M = \Omega(B^{1+\varepsilon})$$

# Scanning in the CO model

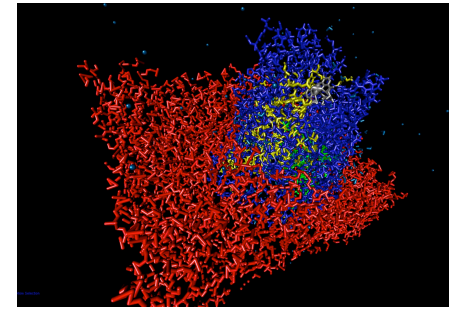


$$W(N) = N$$

$$Q(N) = \lceil N / B \rceil + 1$$



# Spatial Coherency



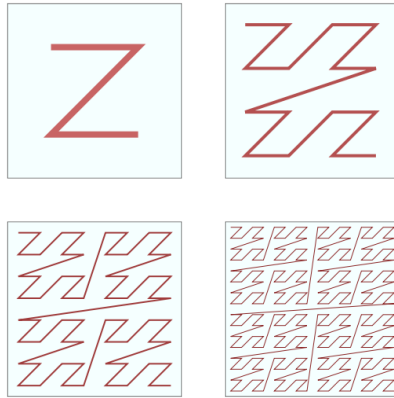
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Try to keep this 3D locality when projecting the data in the 1D memory:

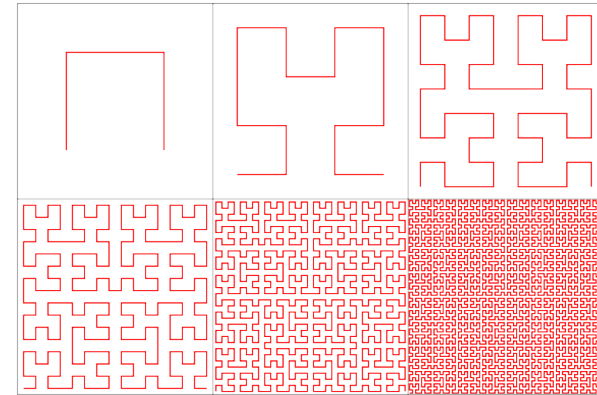
Goal: Access  $n$  neighbor data by  
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# Space-filling Curves



Morton Curve

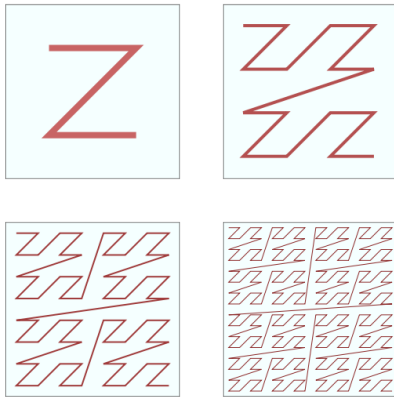
$(x,y,z) \rightarrow Z\text{-index by bit switches}$



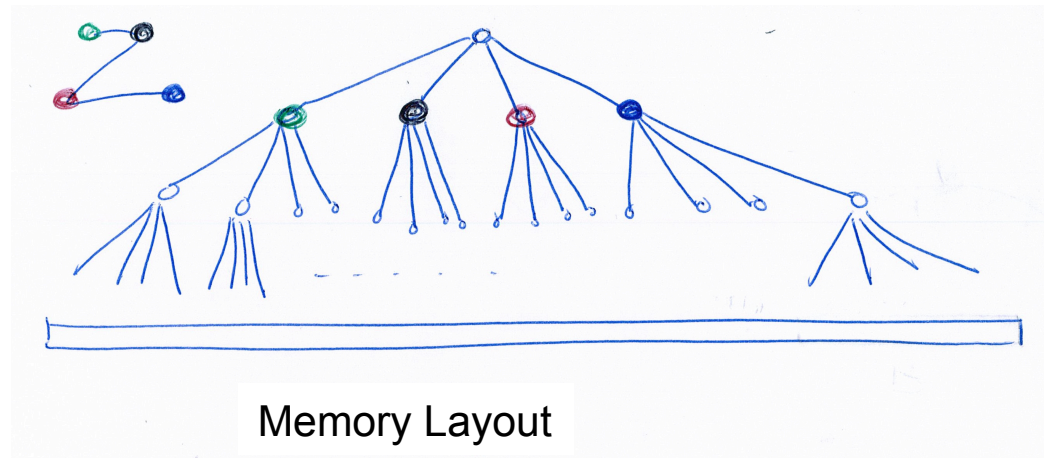
Hilbert Curve

Examples in 2D, but extends to higher dimensions

# Morton Curve

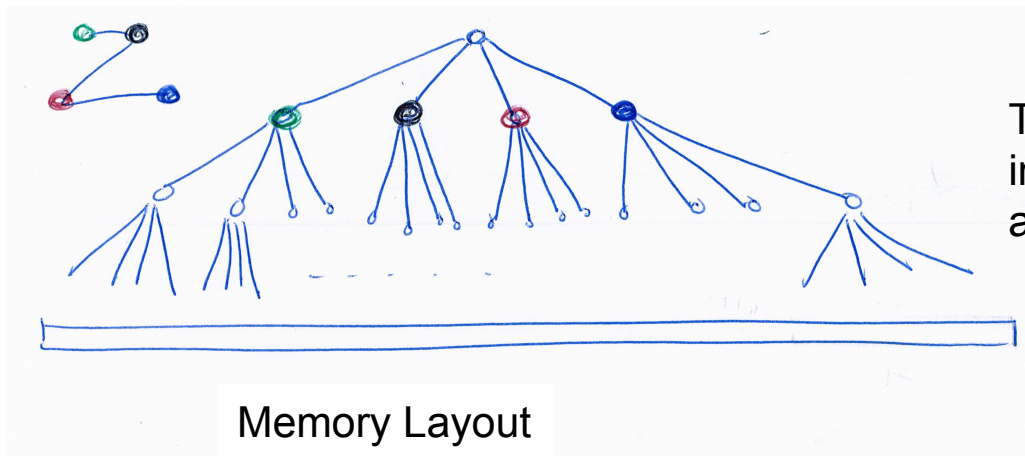


Morton Curve





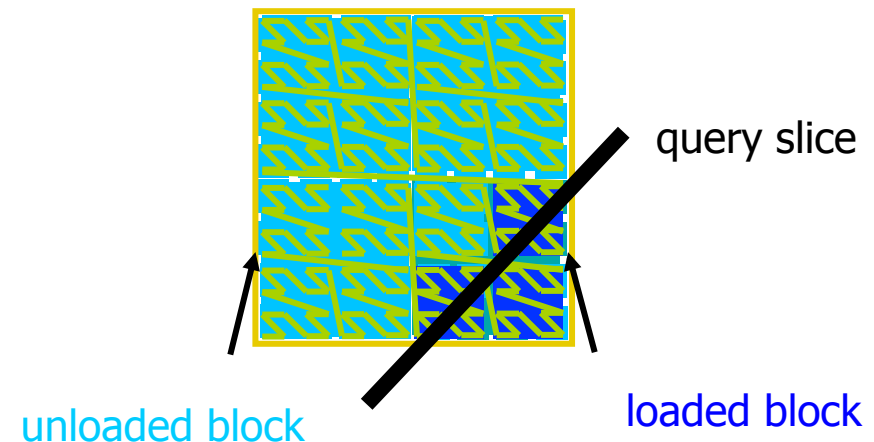
# Morton Curve: Cache Oblivious Data Layouts



The layout is computed independently from a given  $M$  and  $B$

Spatially coherent accesses will show a good cache behavior

See [Pascucci, Siggraph-2005]  
or [Tchiboukdjian, TVCG 2010]  
for mesh specific CO layouts



# Morton Curve Indexing

Z-index obtained by Interleaving the binary coordinates of x and y

	x:	0	1	2	3	4	5	6	7
		000	001	010	011	100	101	110	111
y: 0		000000	000001	000100	000101	010000	010001	010100	010101
1		000010	000011	000110	000111	010010	010011	010110	010111
2		001000	001001	001100	001101	011000	011001	011100	011101
3		001010	001011	001110	001111	011010	011011	011110	011111
4		100000	100001	100100	100101	110000	110001	110100	110101
5		100010	100011	100110	100111	110010	110011	110110	110111
6		101000	101001	101100	101101	111000	111001	111100	111101
7		101010	101011	101110	101111	111010	111011	111110	111111

Z-index

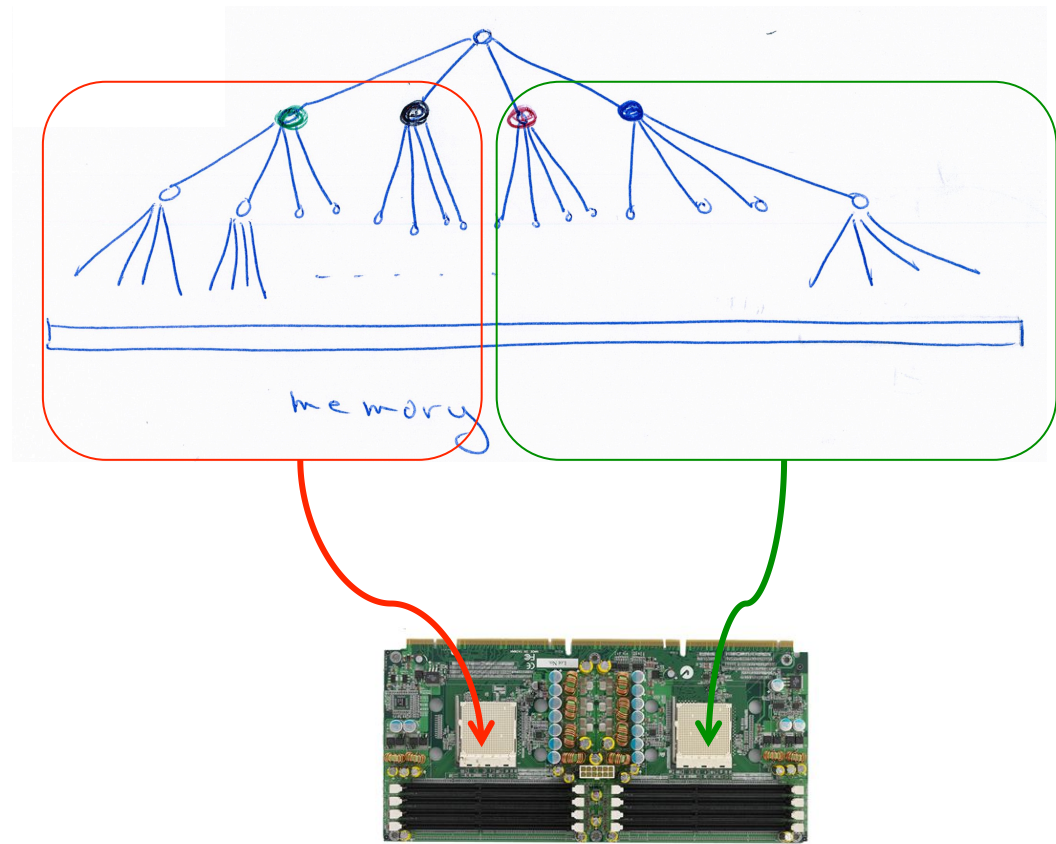
Z-curves are used by some databases data structures (trees, hash tables), or for data partitioning in numerical simulations.

# Morton Curve Based CO layout and Parallelism

- How would you manage such CO layout on a NUMA node ?
- How would you implement parallel element searches ?
- What are the benefits of this CO layout (on a NUMA node) ?

# Morton Curve Based CO layout and Parallelism

- Map data blocks to memory banks according to CO layout
- Make sure threads access local banks first



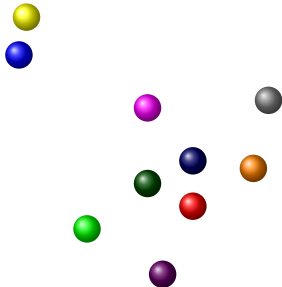
What about GPUs ?

# Packed Memory Array

A Cache Oblivious data structure for dynamics data.

# Cache Oblivious Data Structure for Moving Particles

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# Cache Oblivious Data Structure for Moving Particles

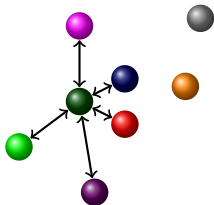
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particles



short-range  
interaction

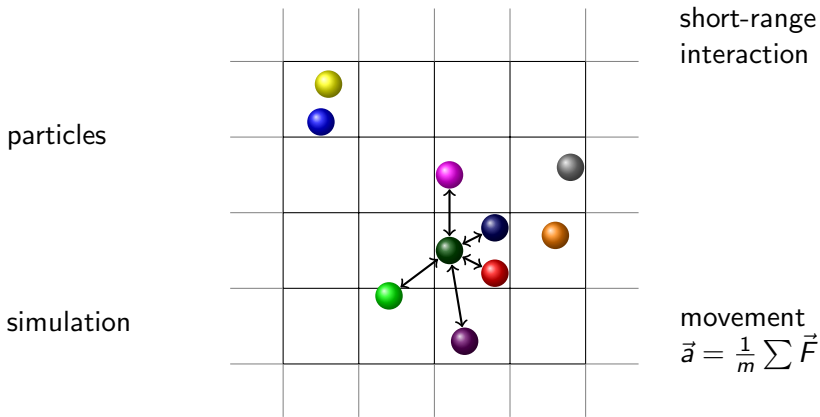
simulation



movement  
$$\vec{a} = \frac{1}{m} \sum \vec{F}$$

# Cache Oblivious Data Structure for Moving Particles

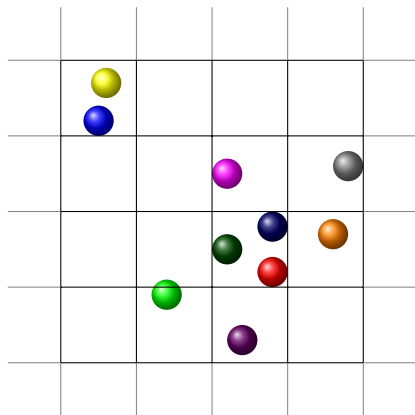
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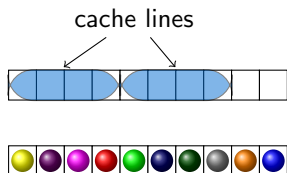


# Spatial Locality Preserving Memory Layout

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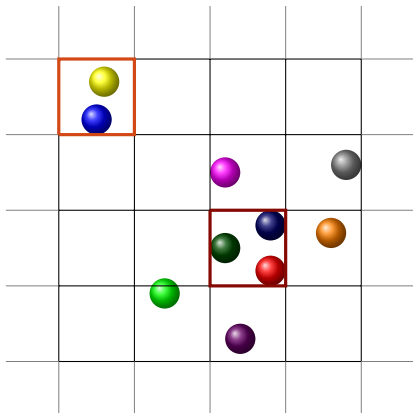
3D Space



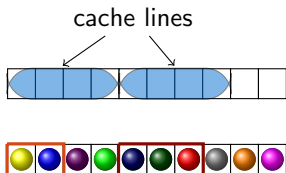
1D memory

# Spatial Locality Preserving Memory Layout

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3D Space

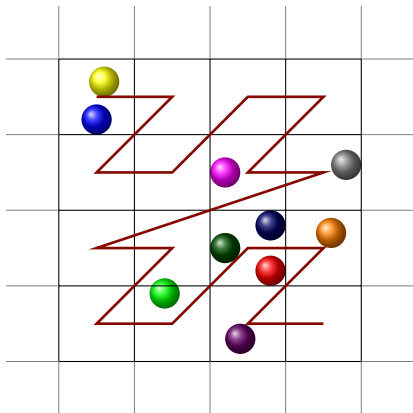


Group particles by cell.

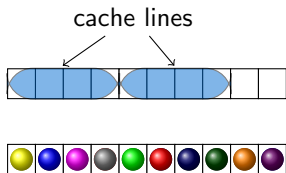
1D memory

# Spatial Locality Preserving Memory Layout

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3D Space



Cell index sorting: **Z-order**

1D memory

# Related Data Structures

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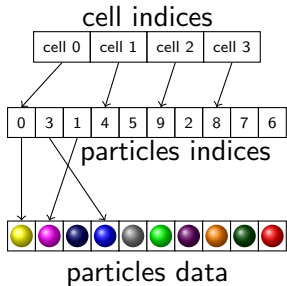
Classical approaches:

- Z-index sorting
- Compact (spatial) hashing

Periodically (1/100):

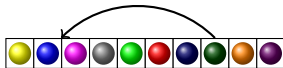
- sort particles data array

[Ihmsen et al., 2011]



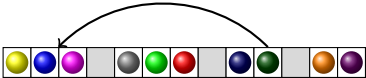
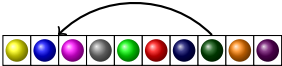
# Idea

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# Idea

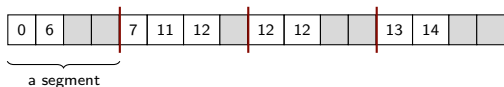
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# Packed Memory Array (PMA)

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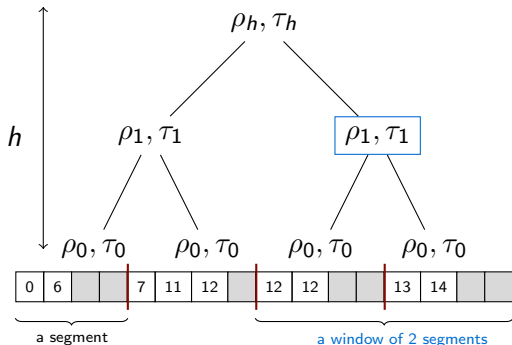
- $K$  elements in an array of size  $N$  ( $N - K$  gaps)
- Segments of size  $\log N$
- #segments: power of 2



[Bender et al. (2000, 2005)]

# Packed Memory Array (PMA)

- Densities min  $\rho$ , max  $\tau$
- $\rho_0 < \dots < \rho_h < \tau_h < \dots < \tau_0$

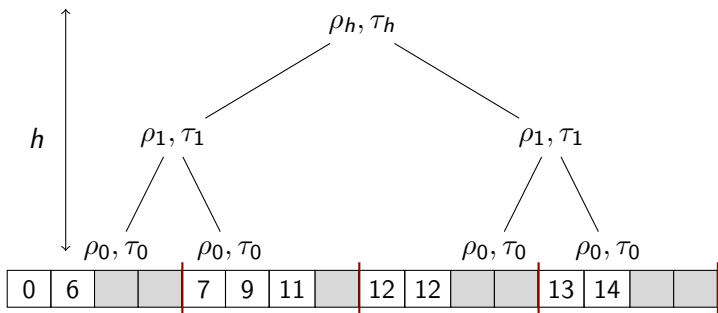


[Bender et al. (2000, 2005)]



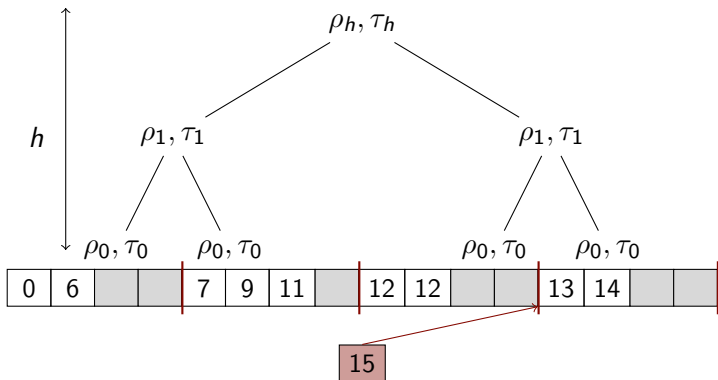
## How Does the Original PMA Work ?

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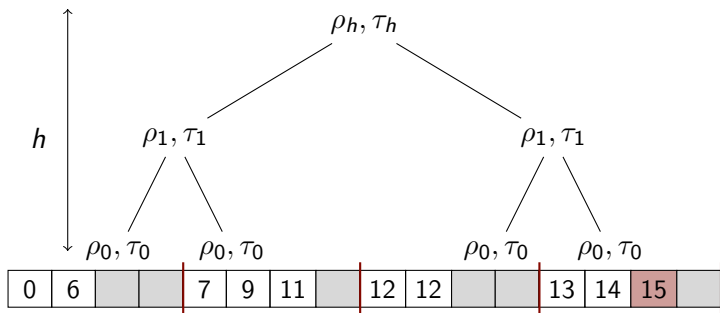
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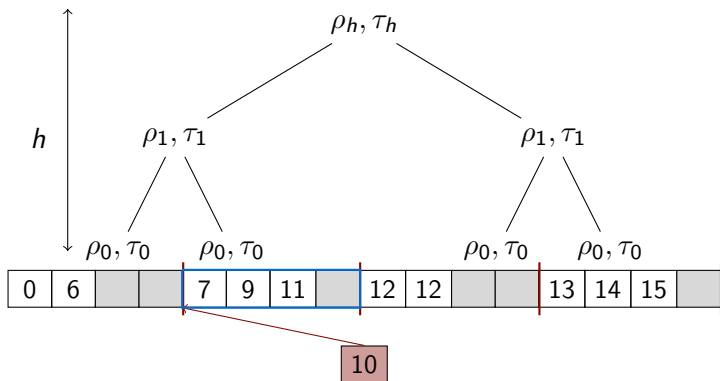
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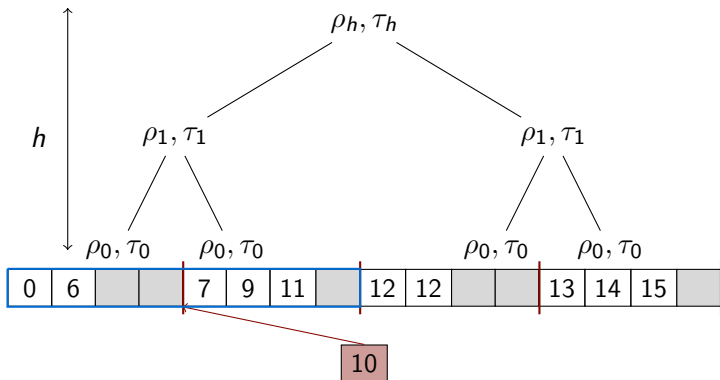
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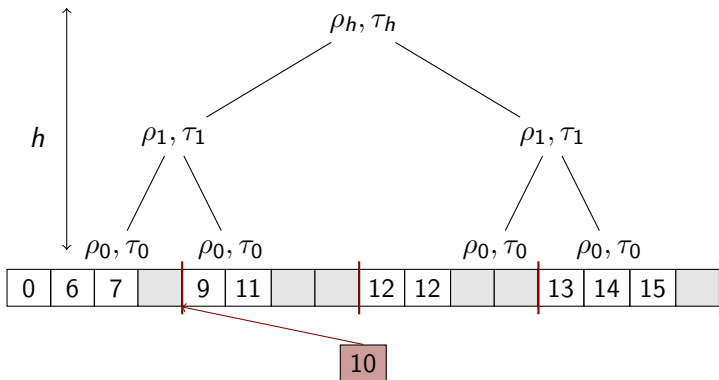
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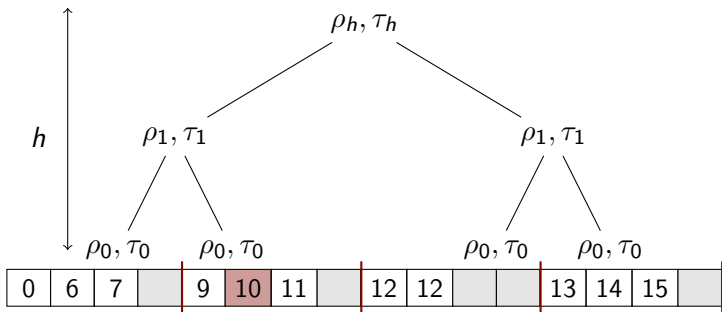
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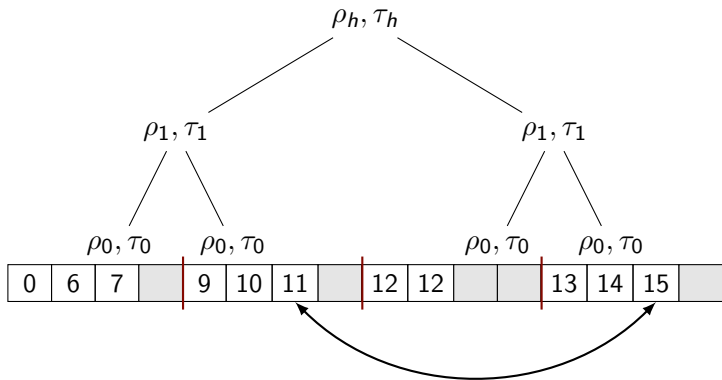
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Amortized number of moves:  $O(\log^2 N)$ .

# Element Moves

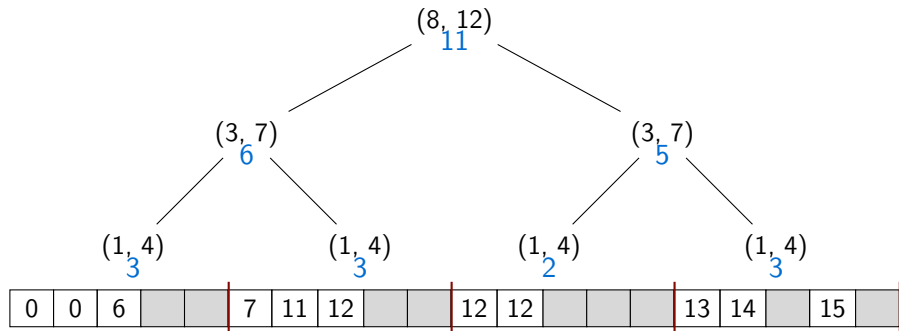
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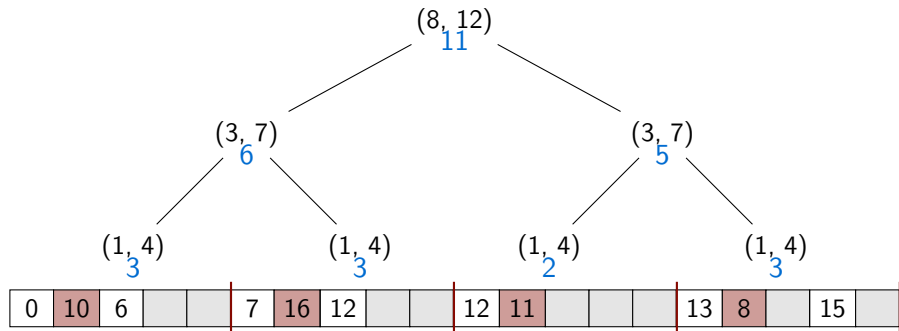
## Applying Moves by Batches

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## Applying Moves by Batches

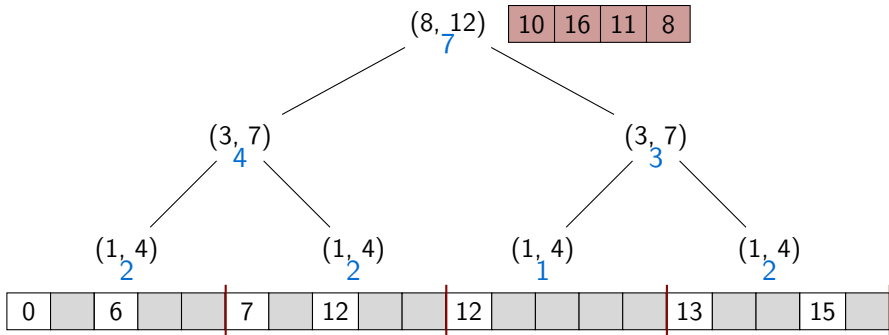
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- 1 Some values change

## Applying Moves by Batches

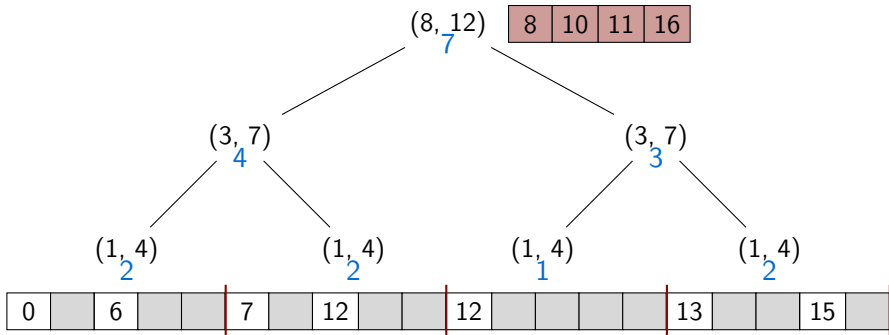
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- 1 Some values change: gather in an array

## Applying Moves by Batches

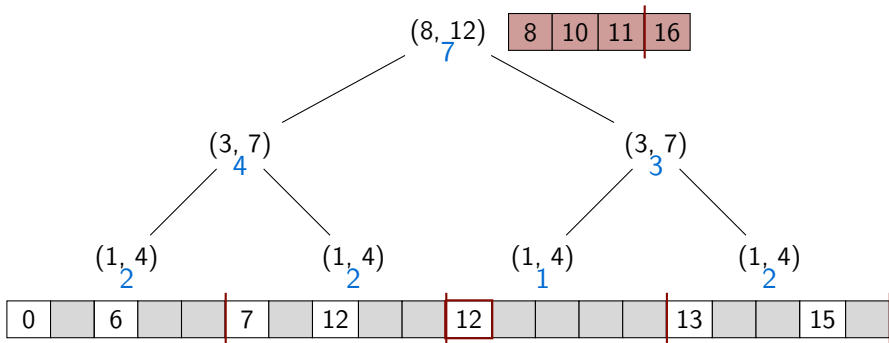
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- 1 Some values change
- 2 Sort of moving elements

## Applying Moves by Batches

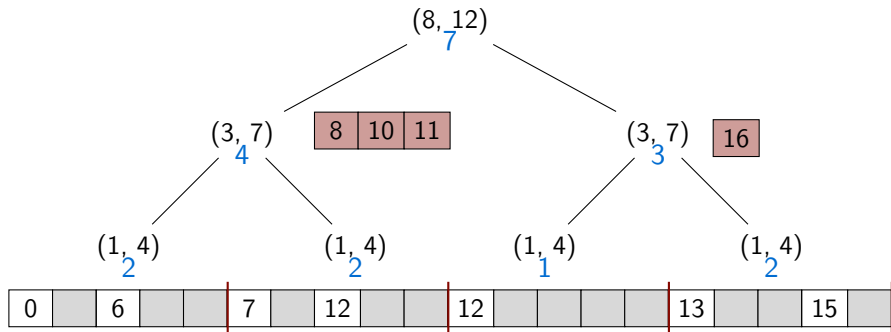
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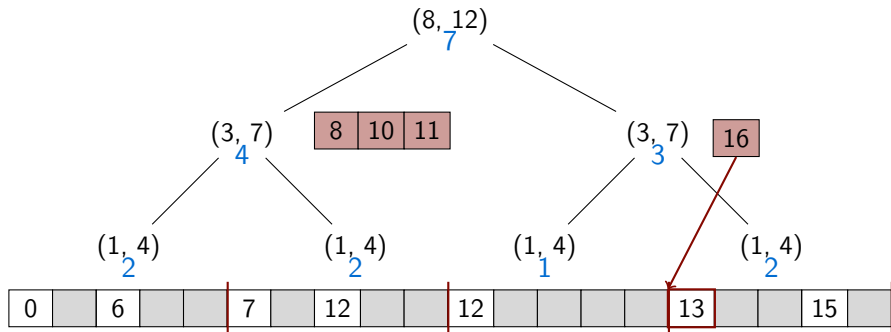
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- 1 Some values change
- 2 Sort of moving elements
- 3 Recursively split array according to window middle value

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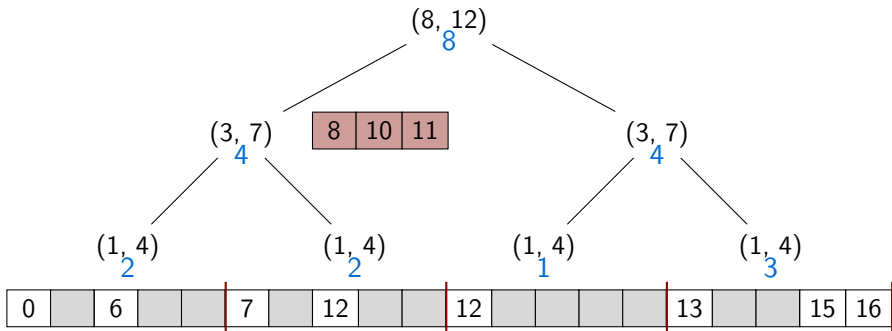
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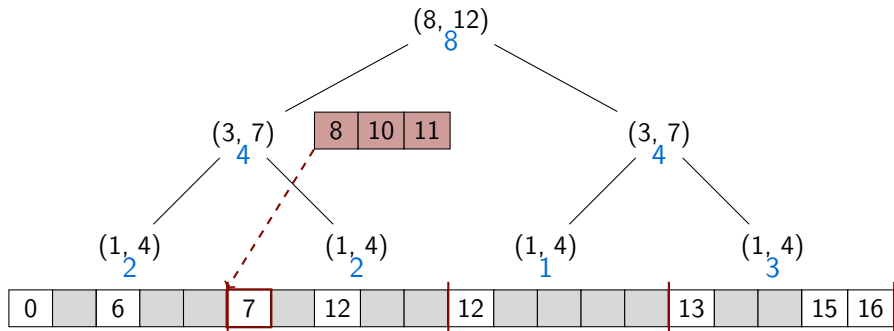


- 1 Some values change
- 2 Sort of moving elements
- 3 Recursively split array according to window middle value
- 4 Direct insertion



## Applying Moves by Batches

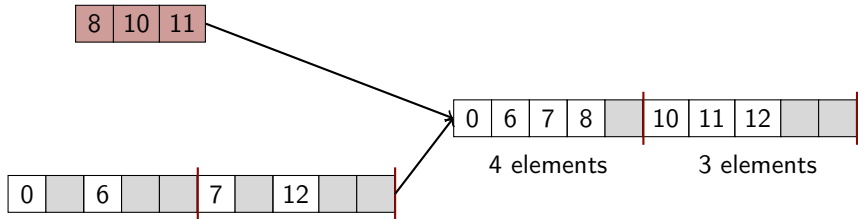
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- 1 Some values change
- 2 Sort of moving elements
- 3 Recursively split array according to window middle value
- 4 Direct insertion or rebalance

## Rebalance with a Single Scan

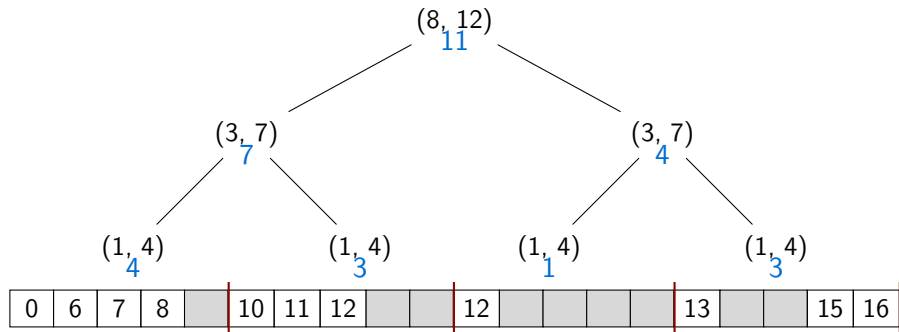
---



Merging two sorted lists: one scan, in place.

## Applying Moves by Batches

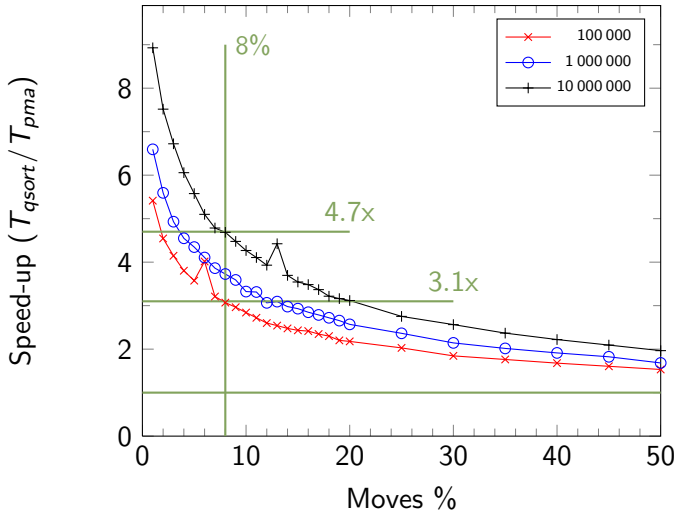
---



Supports moves, insertions and deletions.

# Experimental Results: Moving Integers

## Sorting Moving Elements: Speed-up of PMA vs Qsort (Libc)



$T_{isort} = 500 \times T_{pma}$  with  
100 000 elements and  
10% of moves

Array filled with random  
elements. Execution time  
measured around several  
applications of a given  
percentage of randomly  
selected moves.

## Scan Performance

---

Dense array:

```
for i in 1 to K do
  sum += a[i]
```

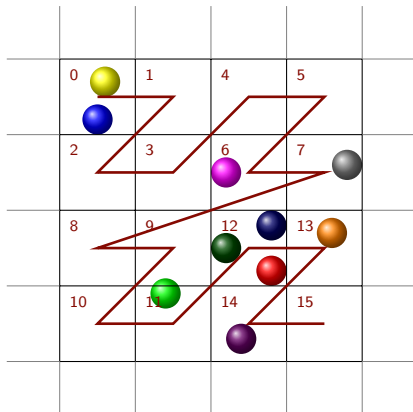
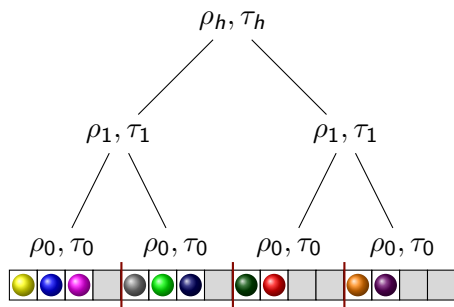
PMA:

```
for i in 1 to N do
  if isValid(a[i])
    sum += a[i]
```

$K$	$N$	$N/K$	$T_{PMA}/T_{array}$
100 000	163 840	1.64	1.69
1 000 000	1 572 864	1.57	1.86
2 900 000	4 456 448	1.54	1.74
10 000 000	15 728 640	1.57	1.78

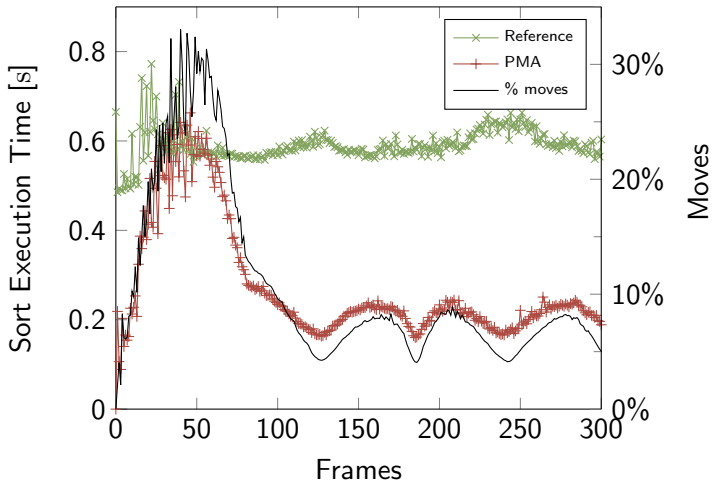
- Test case built to exacerbate the overhead. On realistic computation schemes it fades away.

# Application to Particles



# Application to Particles: Results

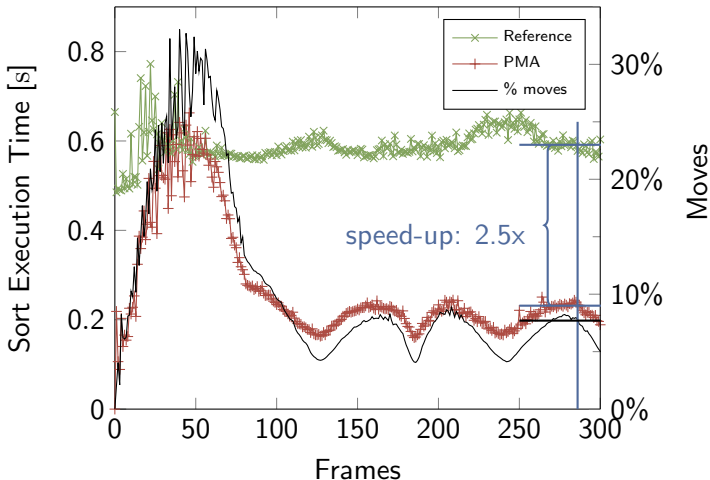
Implementation in Fluids [Hoetzlein, 2008]:  $2.9 \cdot 10^6$  particles



Global performance: 2.8% (sort is 4.5% of total simulation time).

# Application to Particles: Results

Implementation in Fluids [Hoetzlein, 2008]:  $2.9 \cdot 10^6$  particles



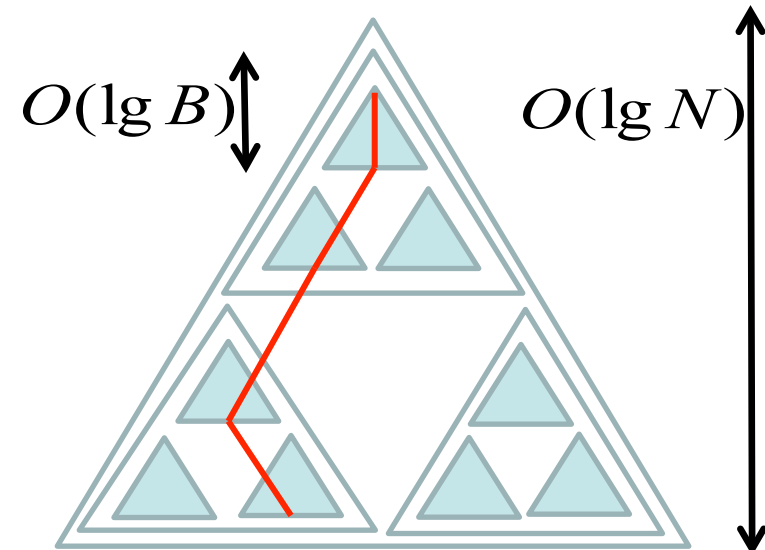
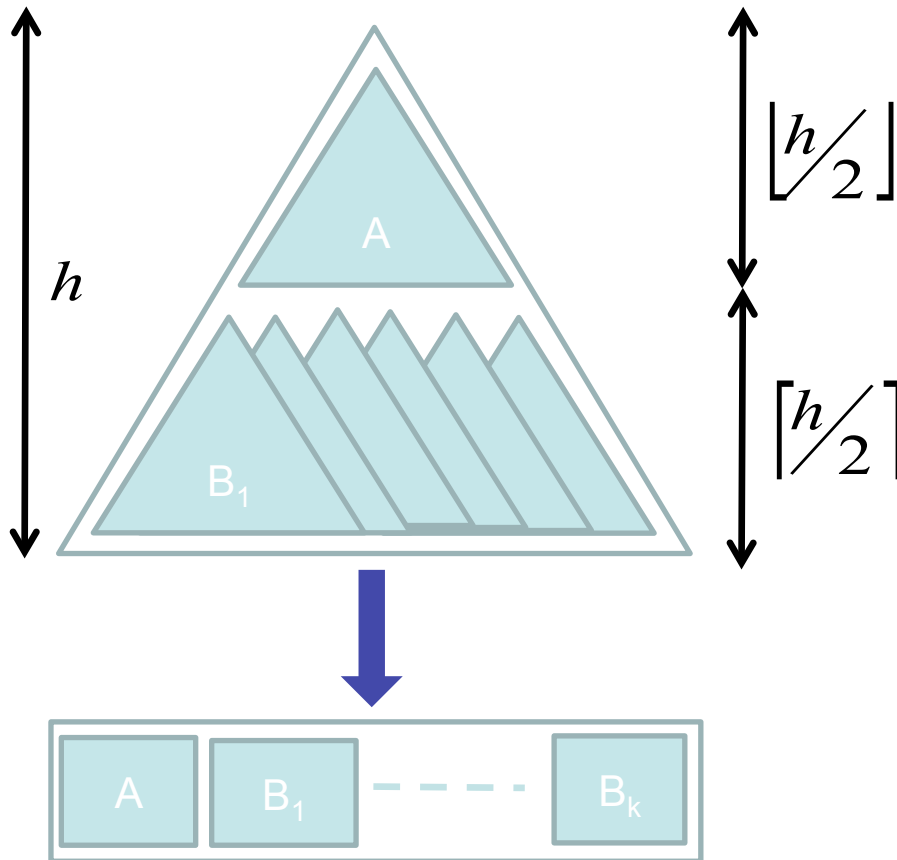
Global performance: 2.8% (sort is 4.5% of total simulation time).



# Searching in the CO model

Binary tree mapped in memory using a recursive layout

[Bender et al 2000]

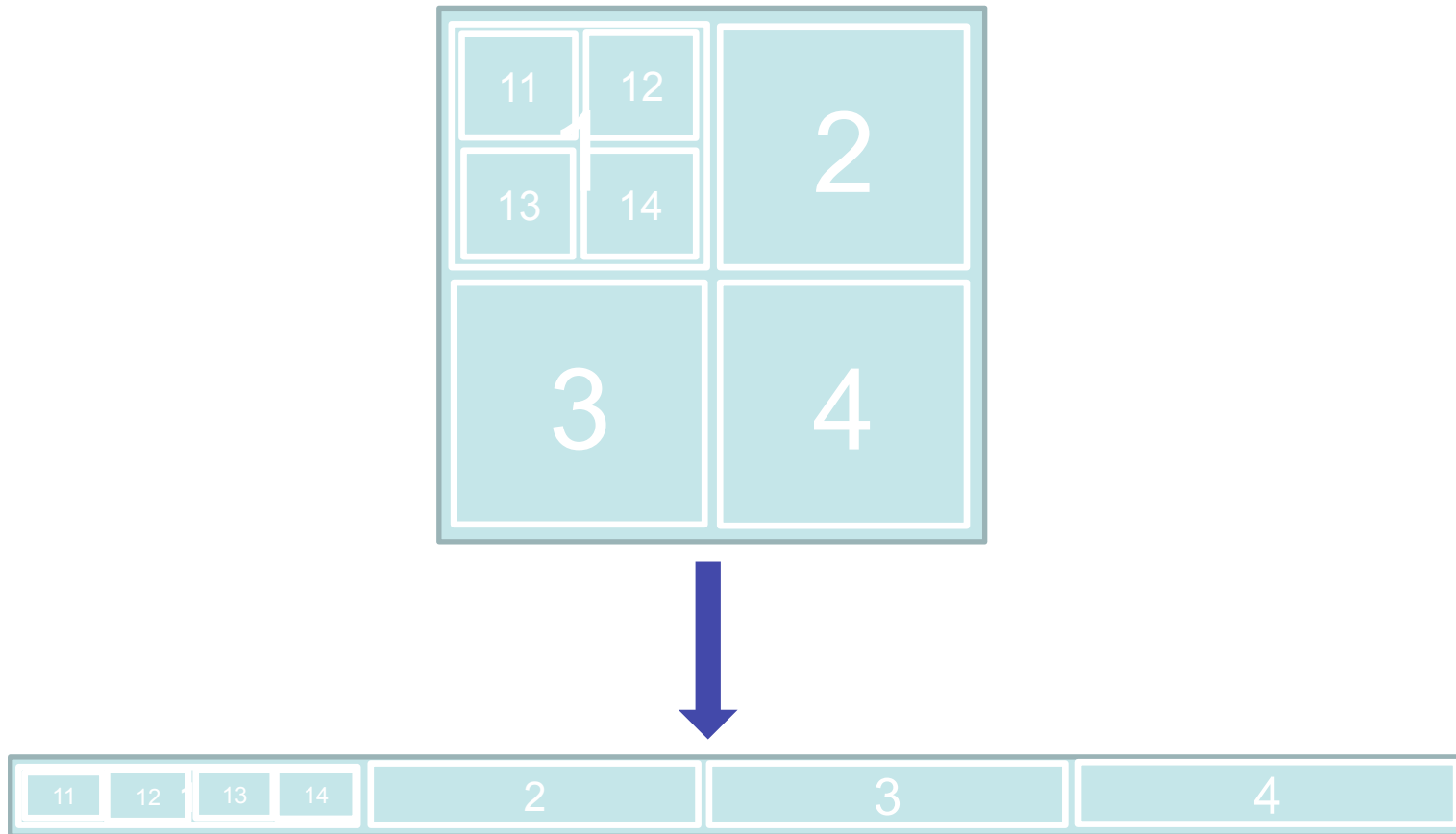


$$W(N) = O(\lg N)$$

$$Q(N) = O(1) \cdot \frac{O(\lg N)}{O(\lg B)} = O(\log_B N)$$

# Multiplying in the CO model

D&C matrix multiplication using a recursive layout



# Multiplying in the CO model

D&C matrix multiplication using a recursive layout

$$W(N) = \begin{cases} 8W(N/2) + O(N^2) & \text{if } N > 1 \\ O(1) & \text{otherwise} \end{cases}$$

$$W(N) = O(N^3)$$

$$Q(N) = \begin{cases} 8Q(N/2) + O(N^2/B) & \text{if } N^2 > M/3 \\ O(N^2/B) & \text{otherwise} \end{cases}$$

$$Q(N) = O\left(\frac{N^3}{B\sqrt{M}}\right)$$

