

# PUF/JLPC Summer school on Performance Metrics, Modeling and Simulation of Large HPC Systems

Sophia Antipolis, France, June 2014



# A few words about organization

8:30 AM Morning session starts

10 AM 5-10 minutes coffee break

12 PM Lunch

1:20 PM Afternoon session starts

3 PM 5-10 minutes coffee break

4:30 PM Afternoon session ends

Then free time to discuss, share ideas/experience, use the swimming pool/have beers together 😊

7:30 PM Dinner at the IBIS hotel



Agnes Cortell

# A few words about the speakers

- 1 HPC Applications Tracing and Analysis  
(Juan Gonzalez, BSC)
- 2 Identifying bottleneck in applications  
(Sanjay Kale and Ronak Buch, UIUC)
- 3 Simulation of HPC systems (Martin Quinson,  
Augustin Degomme, Inria Nancy/Grenoble)
- 4 Reproducible Research (Arnaud Legrand,  
Luka Stanisic, Inria Grenoble)



# A few words about the tutorials

All tutorials will share roughly the same structure:

- 1st hour** General presentation of the problem, trends and possible technical solutions. This is a **related work** presentation.
- 2nd hour** Specific demo of a tool by a specialist
- 3rd hour** Hands on. To this end, we provide QEMU and VirtualBox images

Slides, virtual machine images and instructions on how to use them are available here:

[http://mescal.imag.fr/membres/arnaud.legrand/research/events/puf\\_jlpc\\_workshop\\_14.php](http://mescal.imag.fr/membres/arnaud.legrand/research/events/puf_jlpc_workshop_14.php)

Questions ?